
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: MOSTAFAZADEH

Attorney Docket No.:
NSC1P194D1/P04836D1

Application No.: NEW

Examiner: UNASSIGNED

Filed: HEREWTH

Group: UNASSIGNED

Title: CHIP SCALE PIN ARRAY

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§1.56 AND 1.97(c)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449 may be material to examination of the above-identified patent application. Applicants submit the list of these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application. The above-identified application is a Divisional of prior application U.S. Patent Application No. 09/698,736. This prior application is being relied upon for an earlier filing date under 35 U.S.C. § 120. Because the listed references were either cited by the PTO, or submitted to the PTO in the prior application, under 37 CFR § 1.98(d) Applicants submit that copies need not be provided.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-0388 (Order No. NSC1P194D1).

Respectfully submitted,
BEYER WEAVER & THOMAS, LLP



Phillip P. Lee
Registration No. 46,866

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| Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary) | Atty Docket No. | Application No.: |
| | NSC1P194D1/P04836D1 | NEW |
| | Applicant: | |
| | MOSTAFAZADEH | |
| Filing Date | Group | |
| HEREWITH | UNASSIGNED | |

U.S. Patent Documents

| Examiner Initial | No. | Patent No. | Date | Patentee | Class | Sub-class | Filing Date |
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Foreign Patent or Published Foreign Patent Application

| Examiner Initial | No. | Document No. | Publication Date | Country or Patent Office | Class | Sub-class | Translation | |
|------------------|-----|--------------|------------------|--------------------------|-------|-----------|-------------|----|
| | | | | | | | Yes | No |
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| Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary) | Atty Docket No. | Application No.: |
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| | Applicant: | |
| | MOSTAFAZADEH | |
| | Filing Date | Group |
| | HEREWITH | UNASSIGNED |

Other Documents

| Examiner Initial | No. | Author, Title, Date, Place (e.g. Journal) of Publication |
|------------------|-----|---|
| | 29 | U.S. Patent Application No. 09/528,540, entitled "Leadless Packaging Process Using a Conductive Substrate", filed March 20, 2000, inventor(s): Bayan et al. |
| | 30 | U.S. Patent Application No. 09/698,784, entitled "Flip Chip Scale Package", filed October 26, 2000, inventor(s): Shahram Mostafazadeh |
| | 31 | U.S. Patent Application No. 09/698,736, entitled "Chip Scale Pin Array", filed October 26, 2000, inventor(s): Shahram Mostafazadeh |
| Examiner | | Date Considered |

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.